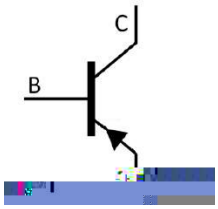
TO-92 PNP Silicon PNP transistor in a TO-92 Plastic Package.

2SC1740

Excellent $h_{FE}$ linearity, complementary pair with 2SC1740.

General purpose applications.

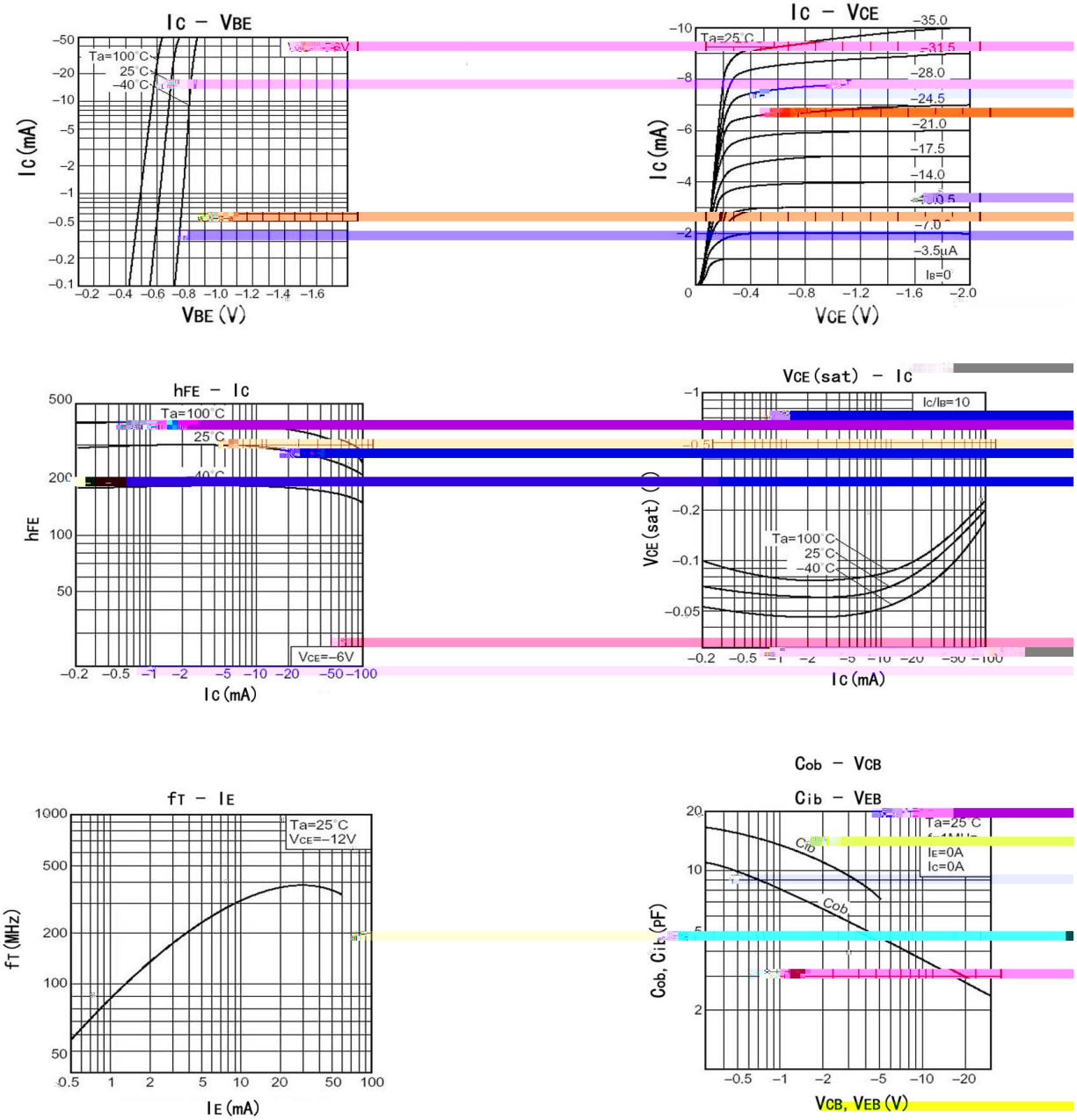




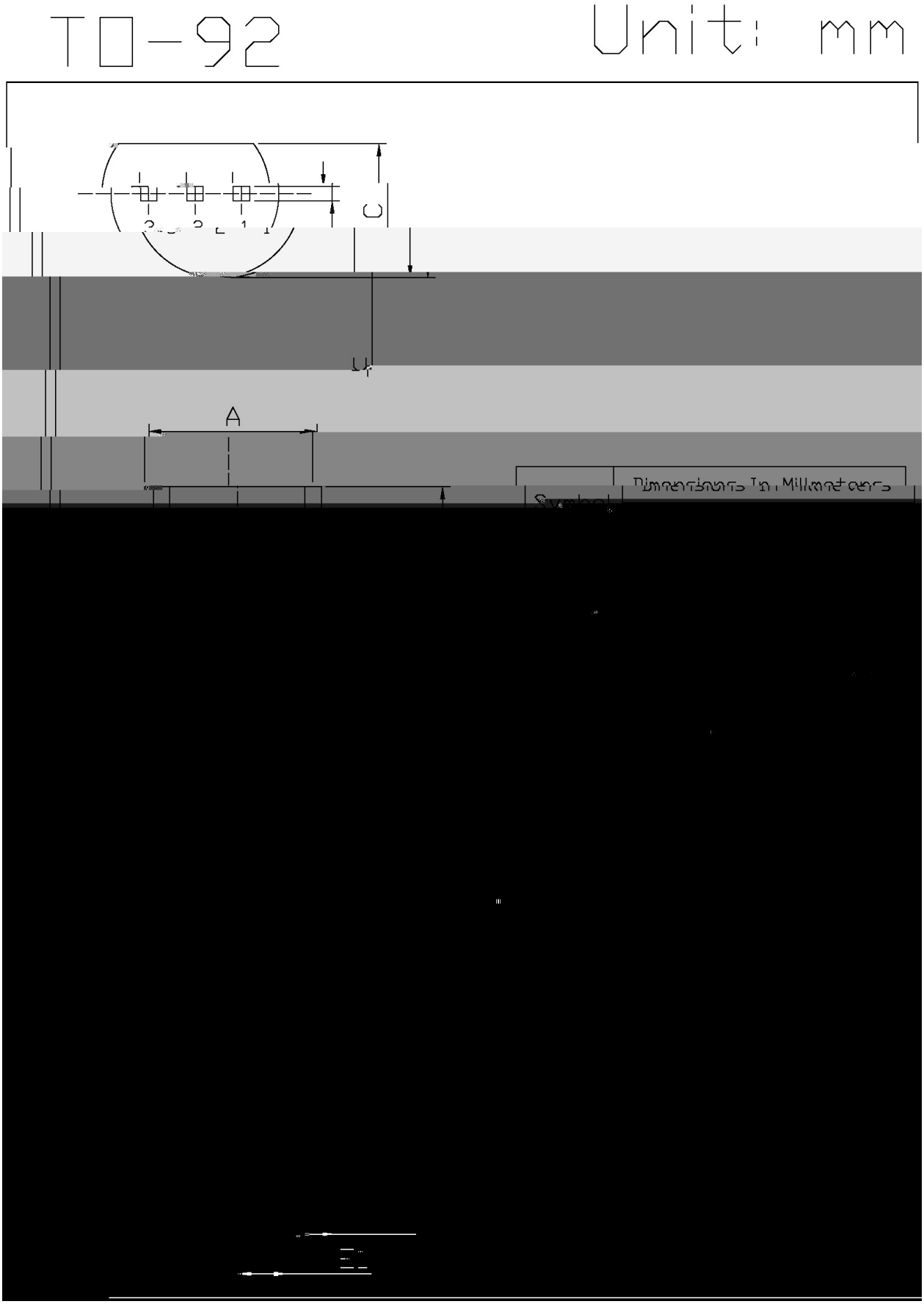

PIN1 Base PIN 2 a3]TJ/TT7 1 Tf79.0971 0TD0 Tc0 Tw8.0797 Tj/TT4 1 Tf1 0 TD-0.0002 Tc1[E)-37(sm)-374(it

| Parameter | Symbol | Rating | Unit |
|---|---|---|---|
| Collector to Base Voltage | $V_{CBO}$ | -60 | V |
| Collector to Emitter Voltage | $V_{CEO}$ | -50 | V |
| Emitter to Base Voltage | $V_{EBO}$ | -6.0 | V |

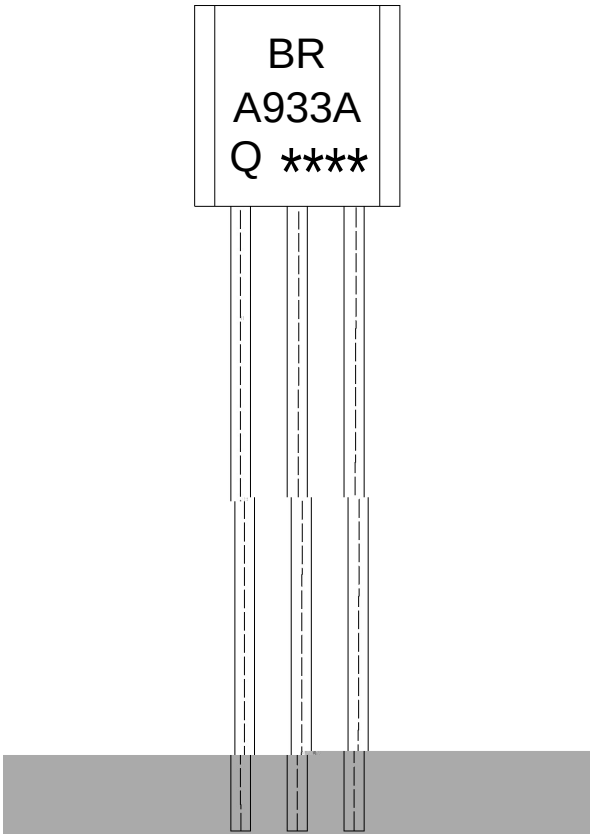
## / Electrical Characteristic Curve



## / Package Dimensions



## / Marking Instructions




BR:

A933A

Q: $h_{FE}$

****

Note:

| | |
|---|---|
| BR: | Company Code. |
| A933A: | Product Type. |
| Q: | $h_{FE}$ Classifications Symbol. |
| ****: | Lot No. Code,code change with Lot No. |

## ( ) / Temperature Profile for Dip Soldering(Pb-Free)



| | | | | |
|---|---|---|---|---|
| | | | | Note: |
| 1 | 25 | 150 | 60 90sec; | 1.Preheating:25~150 , Time:60~90sec. |
| 2 | 255±5 | | 5±0.5sec; | 2.Peak Temp.:255±5 , Duration:5±0.5sec. |
| 3 | | 2 10 | /sec. | 3. Cooling Speed: 2~10 /sec. |

[redacted]; c.