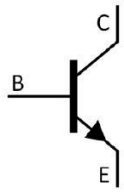
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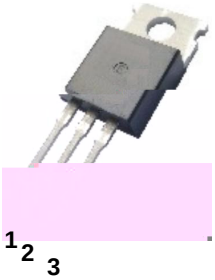
/ Features

/ Applications

/ Equivalent Circuit

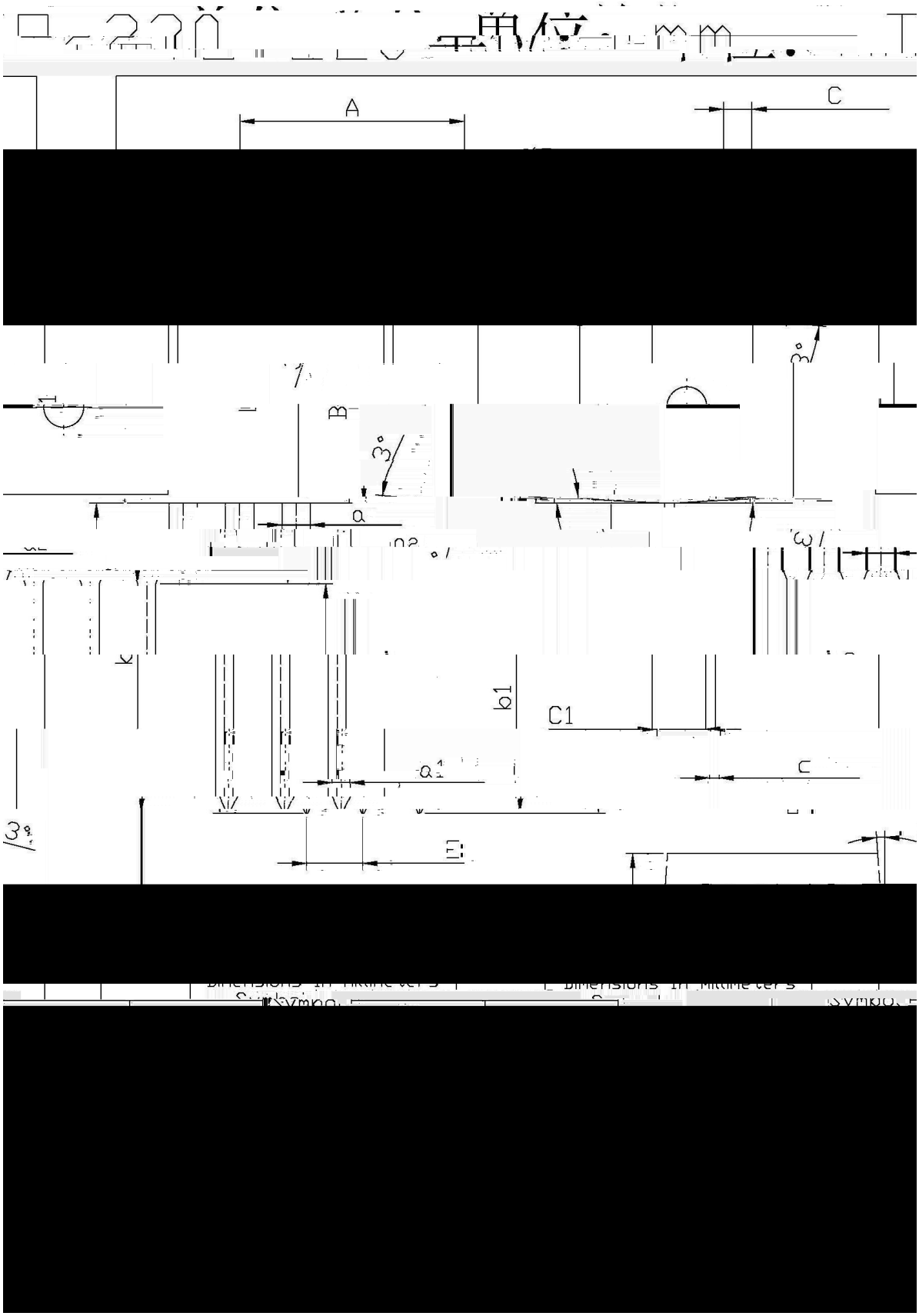


/ Pinning

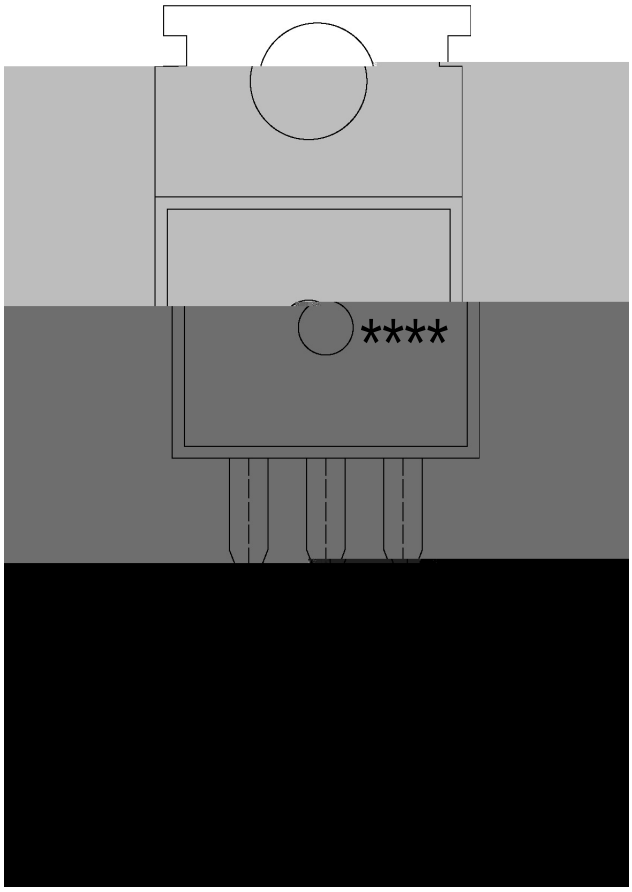


/ h_{FE} Classifications & Marking

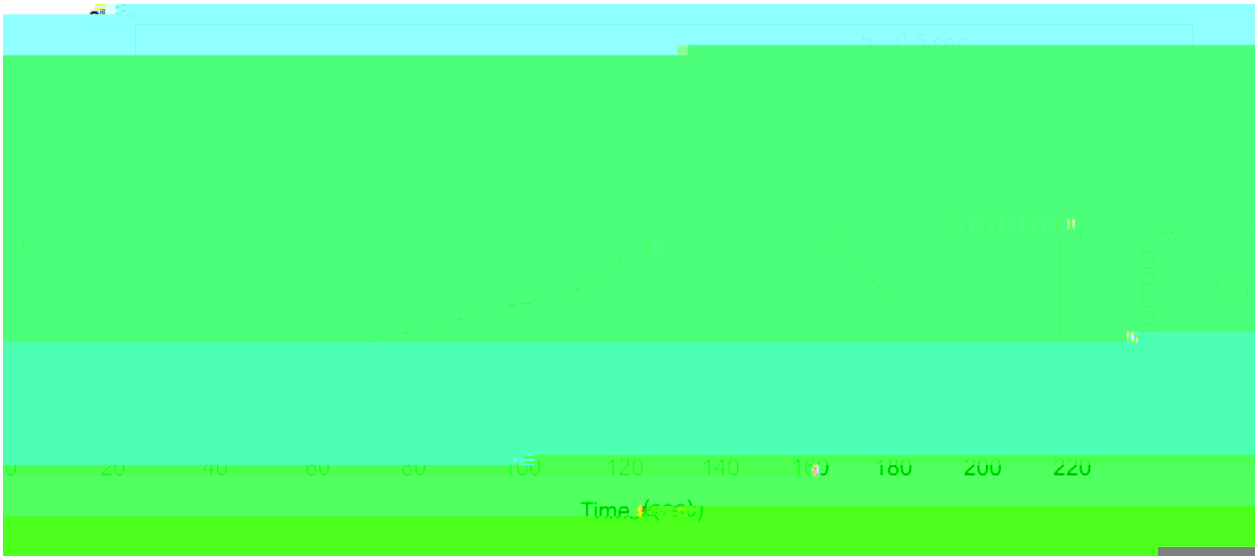
/ Package Dimensions



/ Marking Instructions



( ) / Temperature Profile for Dip Soldering(Pb-Free)



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