/ Descriptions

SOP-8 P MOS

P-Channel Enhancement Mode Field Effect Transistor in a SOP-8 Plastic Package.

/ Features

V_{DS} (V) = -30V I_D = -6.9A (V_{GS} = ± 20V)

$R_{DS(ON)}$@-10V≤30mΩ(Typ.28.5mΩ)

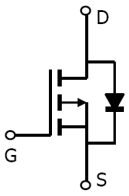
$R_{DS(ON)}$@-4.5V≤55mΩ(Typ.40mΩ)

HF Product.

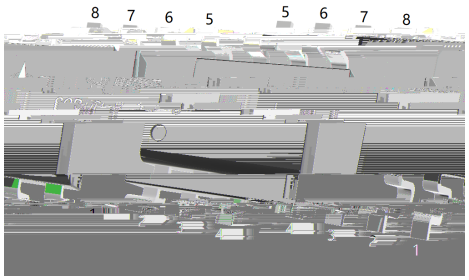
/ Applications

Power Management in Notebook computer, Portable Equipment and Battery powered systems.

/ Equivalent Circuit



/ Pinning



PIN1	S	PIN 2	S	PIN 3	S	PIN 4	G
PIN 5	D	PIN 6	D	PIN 7	D	PIN 8	D

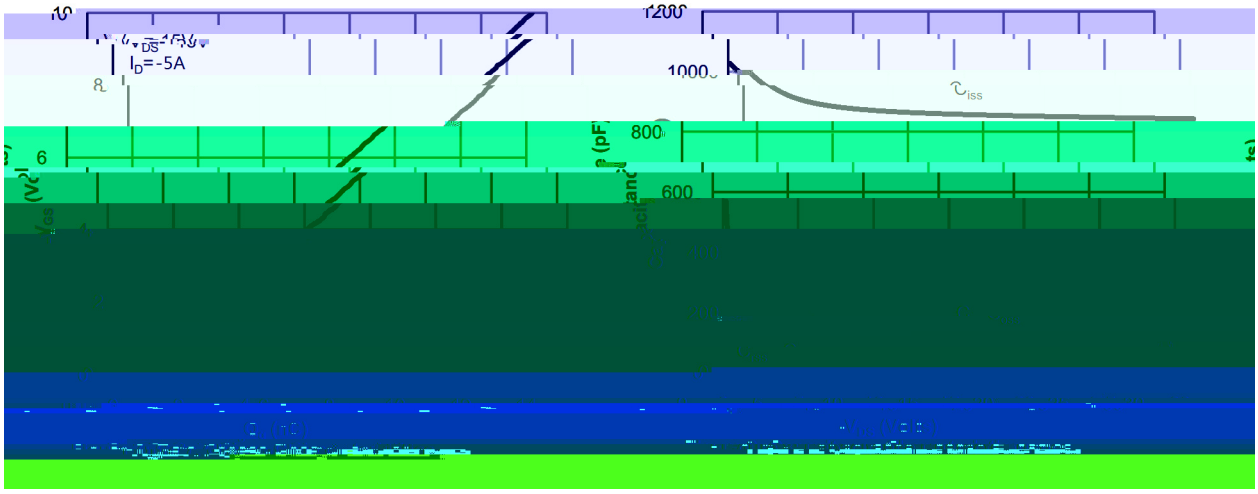
/ Marking

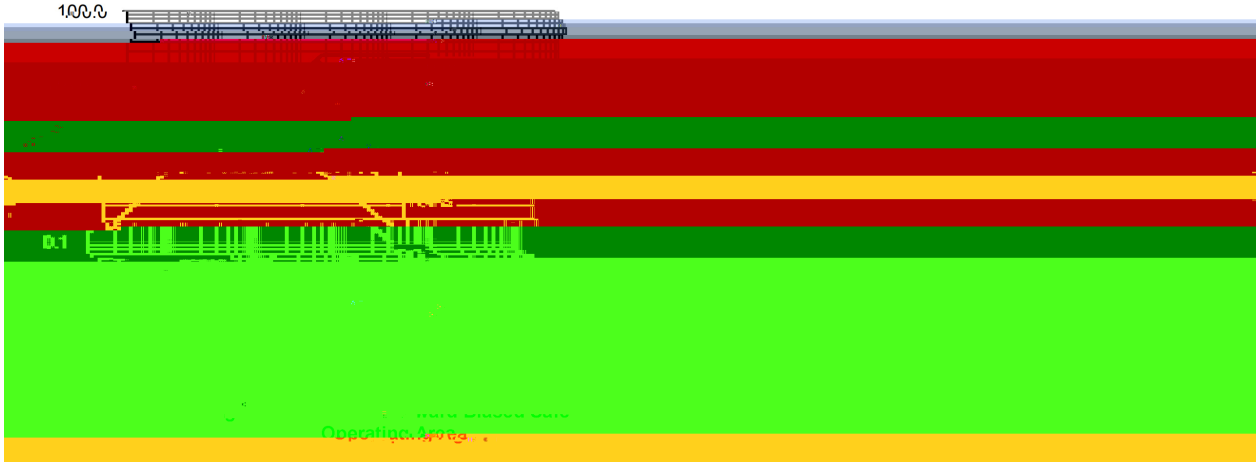
See Marking Instructions.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	I_D=-250μA V_{GS}=0V	-30	-34		V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}				

/ Electrical Characteristic Curve

/ Electrical Characteristic Curve







/ Package Dimensions

