/ Revised record

/ Descriptions

SOT-23 PNP Silicon PNP Digital transistor in a SOT-23 Plastic Package.

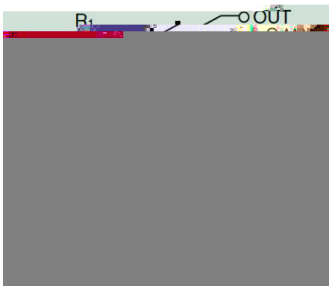
/ Features

With built-in bias resistors, simplify circuit design, reduce a quantity of parts and manufacturing process,HF Product.

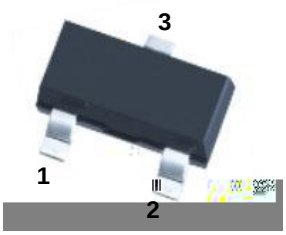
/ Applications

Switching applications, interface circuit and driver circuit applications.

/ Equivalent Circuit



/ Pinning

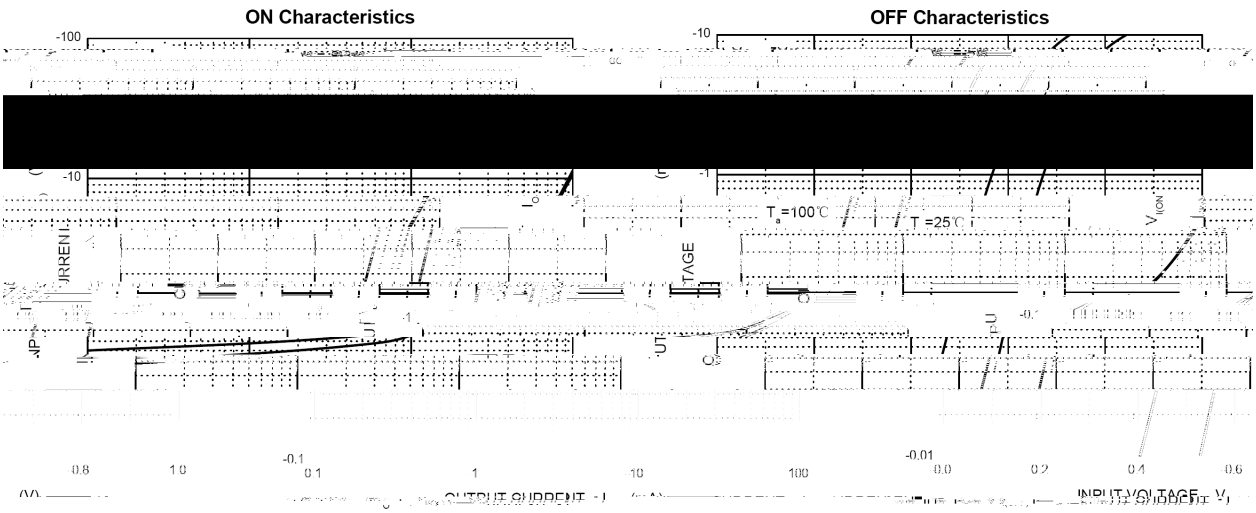


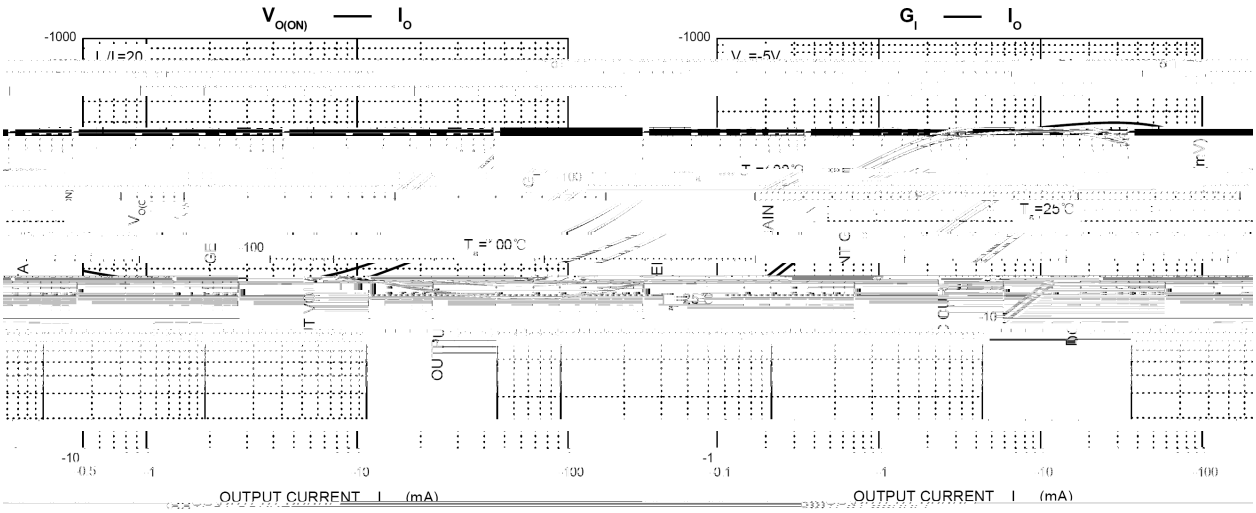
PIN 1 IN PIN 2 GND PIN 3 OUT

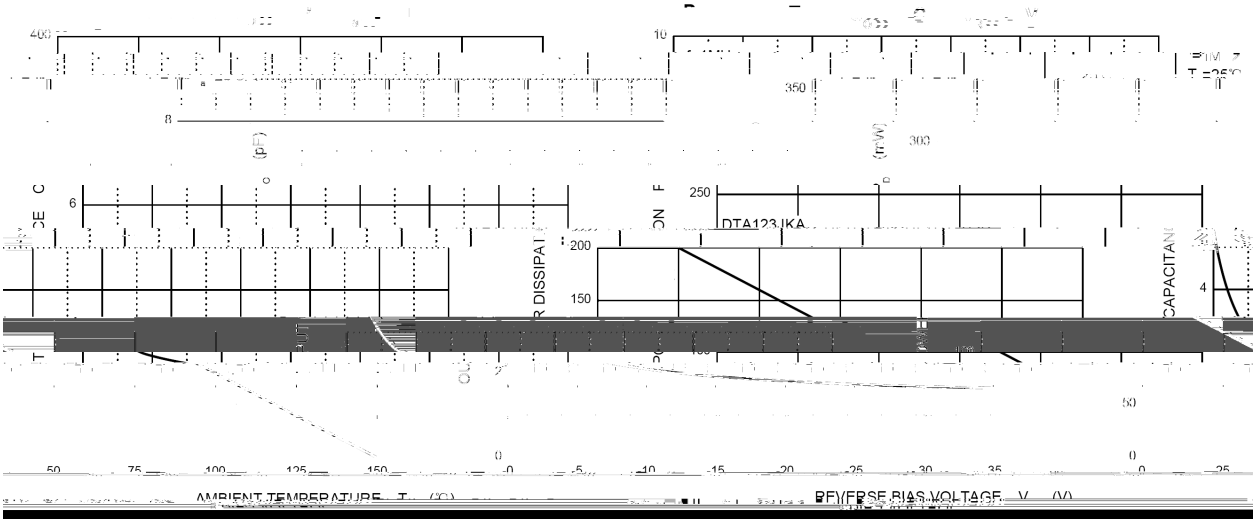
/ Marking

Marking	HE32

/ Electrical Characteristic Curve







http://www.fsbrec.com

( ) / Temperature Profile for IR Reflow Soldering(Pb-Free)

				Note:
1	150	180	60 90sec;	1.Preheating:150~180℃, Time:60~90sec.
2	245±5		5±0.5sec;	2.Peak Temp.:245±5℃, Duration:5±0.5sec.
3		2	10℃/sec.	3. Cooling Speed: 2~10℃/sec.

/ Resistance to Soldering Heat Test Conditions

260±5℃	10±1 sec.	Temp.:260±5℃	Time:10±1 sec

/ Packaging SPEC.

/ REEL

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm^3)		
	Units/Reel /	Reels/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Reel	Inner Box 盒	Outer Box 箱
SOT-23	3,000	10	30,000	6	180,000	7″ ×8	180×120×180	390×385×205