/ Descriptions

WR0<5+U, ṂṂ Ƭ QSQ ÆṀ ч s ⅼ ㄱ Silicon NPN transistor in a TO-92(R) Plastic Package.

/ Features

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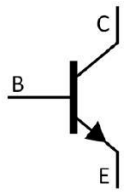
High h_{FE},1 watts amplifier applications, complementary pair with ED1802.

/ Applications

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Audio frequency amplifier applications.

/ Equivalent Circuit



/ Pinning



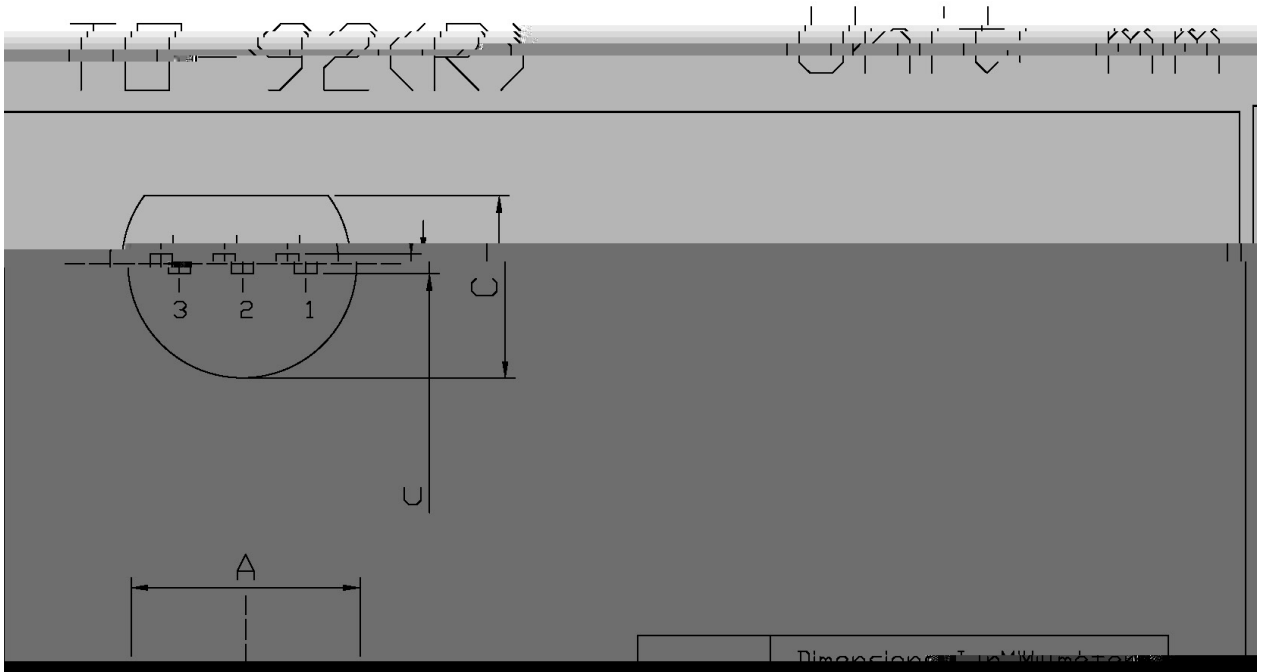
1 2 3

PIN1 χ Emitter PIN 2 χ Base PIN 3 χ Collector

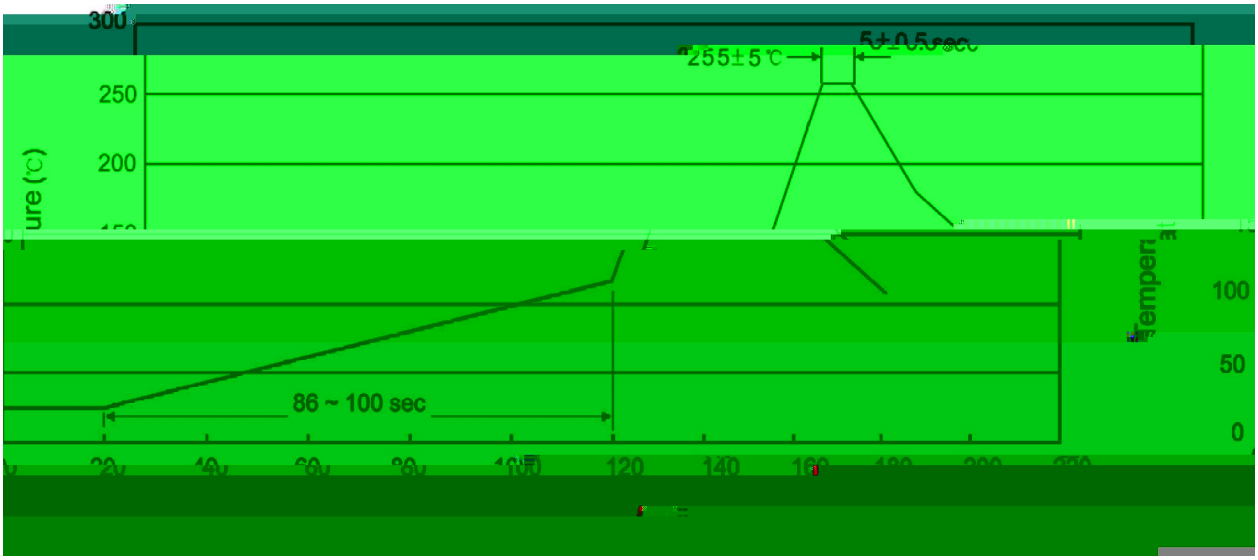
/ h_{FE} Classifications & Marking

h_{FE} Classifications Symbol	K	L	M	N
h_{FE} Range	106 150	132 188	170 230	213 300

/ Package Dimensions



( ) / Temperature Profile for Dip Soldering(Pb-Free)



ب ˝ χ

1 Ĺ ♬ □ ỷ 25 Ḱ 150 ł ı ˋH ڑ 60 Ḱ 90sec;

2 Š Ө □ ỷ 255 ° 5 ł ı ˋH ڑ ˋY ﾊ ㎛ 5 ° 0.5sec;

3 ì ˙ ﾘ ˛ ɔ ﻉ ỷ ㎛ 2 Ḱ 10 ł /sec.

Note:

1.Preheating:25~150 ł , Time:60~90sec.

2.Peak Temp.:255 ° 5 ł , Duration:5 ° 0.5sec.

3. Cooling Speed: 2~10 ł /sec.

/ Resistance to Soldering Heat Test Conditions

□ ỷ χ 270 ° 5 ł	ˋH ڑ χ 10 ° 1 sec.	Temp.:270±5	Time:10±1 sec

/ Packaging SPEC.

ˈQ ţ ﾔ Ł / BULK