/ Descriptions

Silicon NPN transistor in a TO-92 Plastic Package.

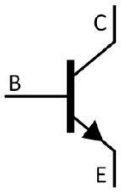
/ Features

Excellent h_{FE} linearity, complementary pair with KTA200.

/ Applications

General amplifier and switching application.

/ Equivalent Circuit



/ Pinning



PIN1 Base PIN 2 Collector PIN 3 Emitter

/ hFE Classifications & Marking

h_{FE} Classifications Symbol	O	Y
h_{FE} Range	70~140	120~240

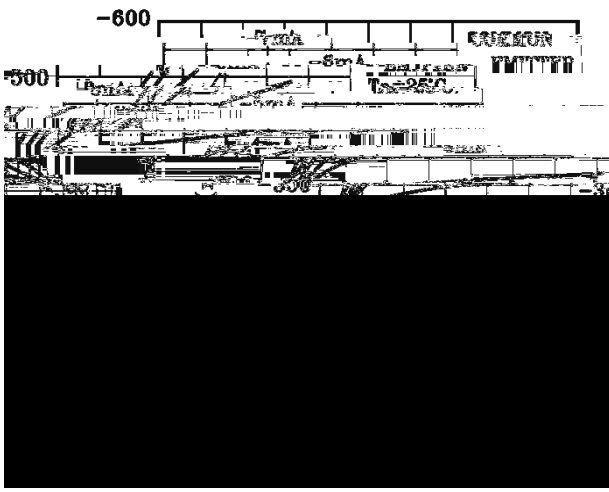
Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	60	V
Collector to Emitter Voltage	V_{CEO}	45	V
Emitter to Base Voltage	V_{EBO}	5.0	V
Collector Current - Continuous	I_C	500	mA
Emitter Current - Continuous	I_E	-500	mA
Collector Power Dissipation	P_C	625	mW
Junction Temperature	T_j	150	
Storage Temperature Range	T_{stg}	-55 150	

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector Cut-Off Current	I_{CBO}	V_{CB}=50V I_E=0			0.1	A
Emitter Cut-Off Current	I_{EBO}	V_{EB}=5.0V I_C=0			0.1	A
DC Current Gain	$h_{FE(1)}$	V_{CE}=2.0V I_C=50mA	70		240	
	$h_{FE(2)}$	V_{CE}=2.0V I_C=200mA	25			

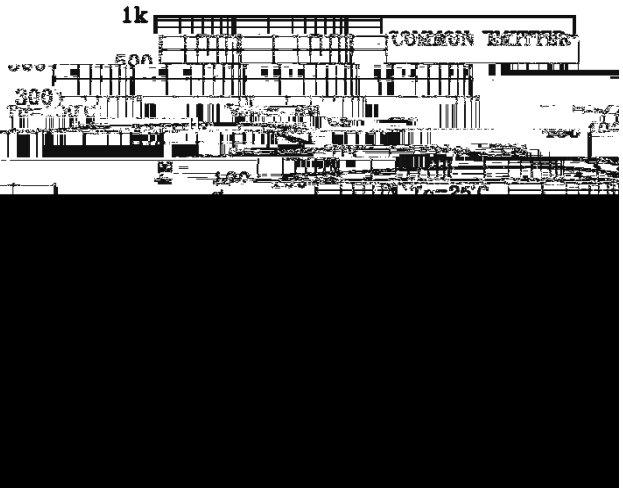
Collector to Emitter Saturation

/ Electrical Characteristic Curve

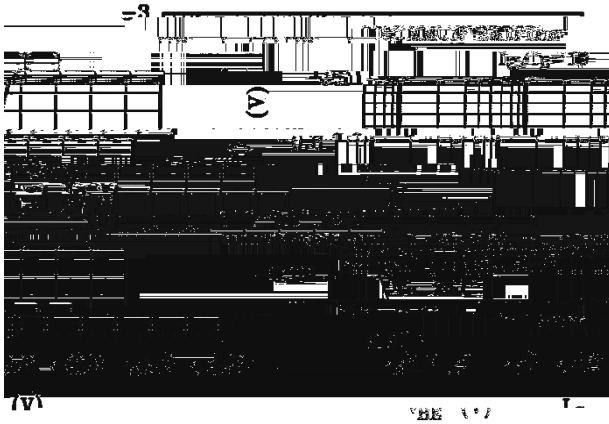
I_C – V_{CE}



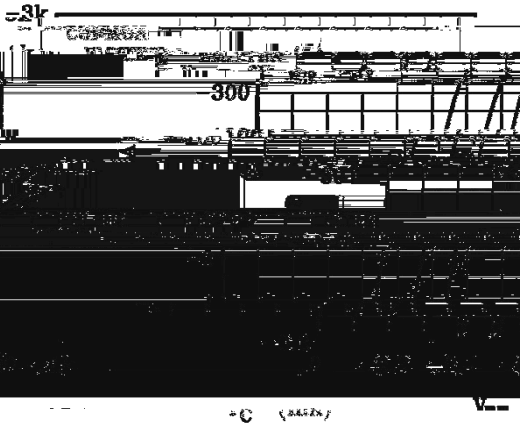
h_{FE} – I_C



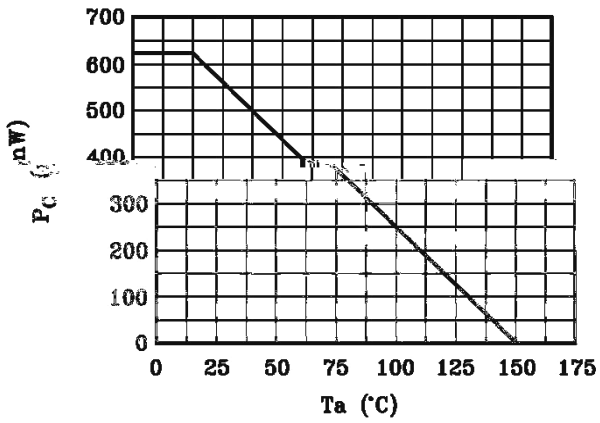
$V_{CE(sat)}$ – I_C



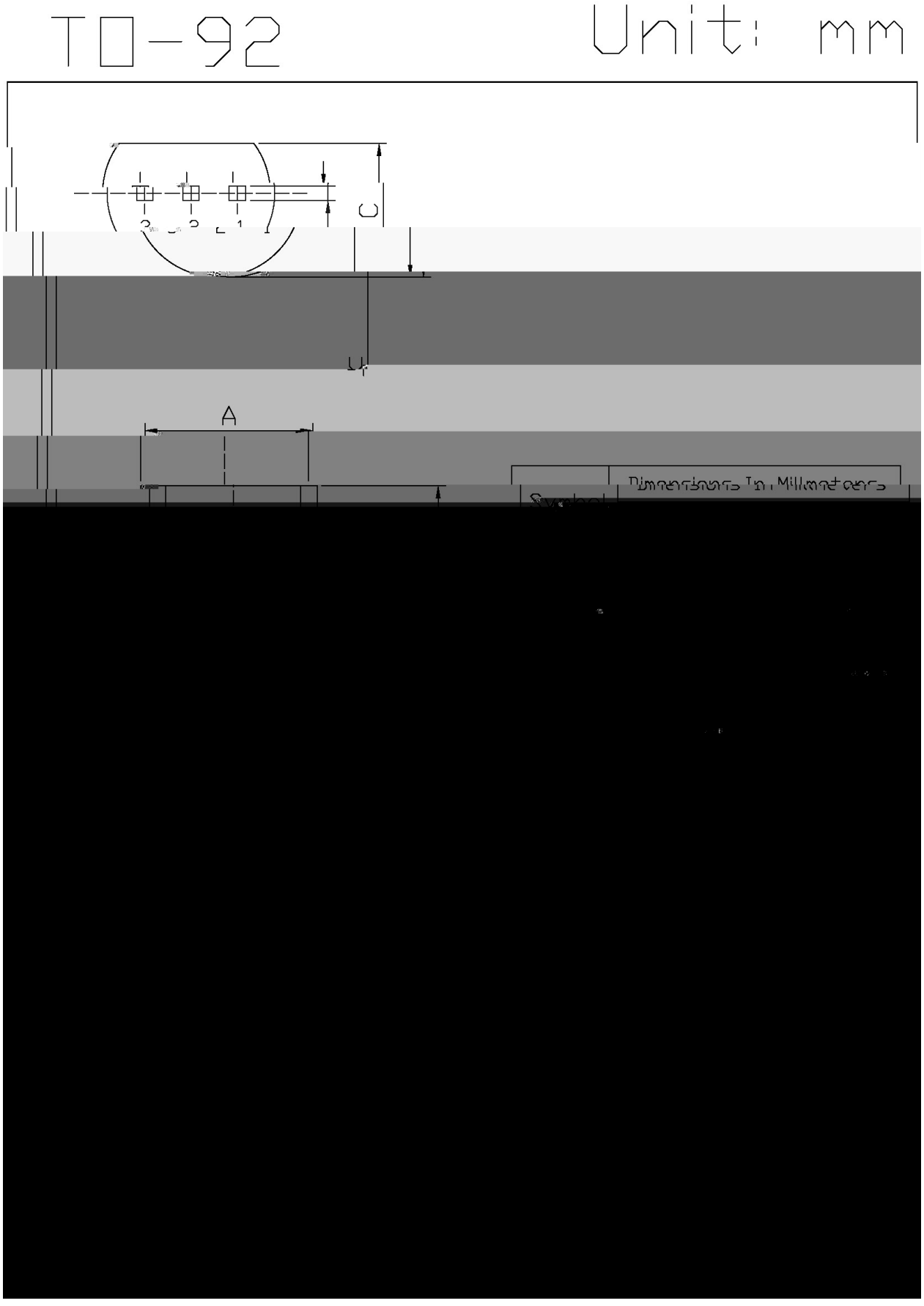
I_B – V_{BE}



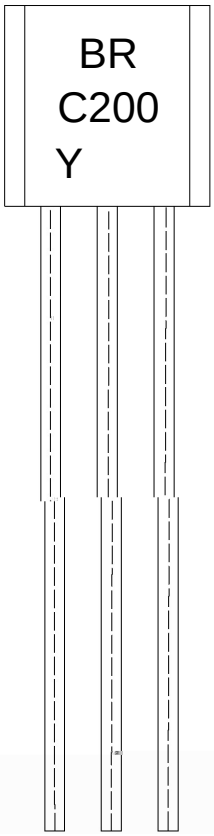
P_C – T_a



/ Package Dimensions



/ Marking Instructions



Note:

BR:	Company Code.
C200:	Product Type.
Y:	h_{FE} Classifications Symbol
****:	Lot No. Code,code change with Lot No.

( ) / Temperature Profile for Dip Soldering(Pb-Free)



Note: