5 é / Descriptions

9 5 : .> / / x 4 6 4 ! - } â ’ož Silicon NPN transistor in a SOT-23 Plastic Package.

¤ ª / Features

2 3 ; mož

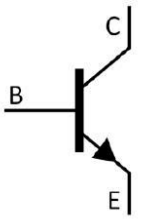
Complementary pair with L8550M.

Đ ÷ / Applications

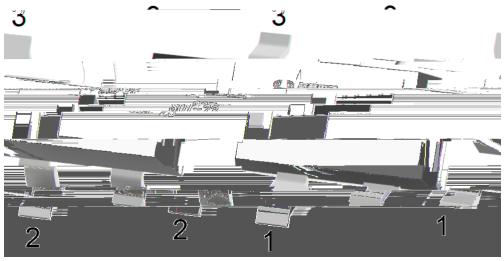
9 ä y Ý èož

Power amplifier applications.

Ã W] Ô · / Equivalent Circuit



• Û - æ

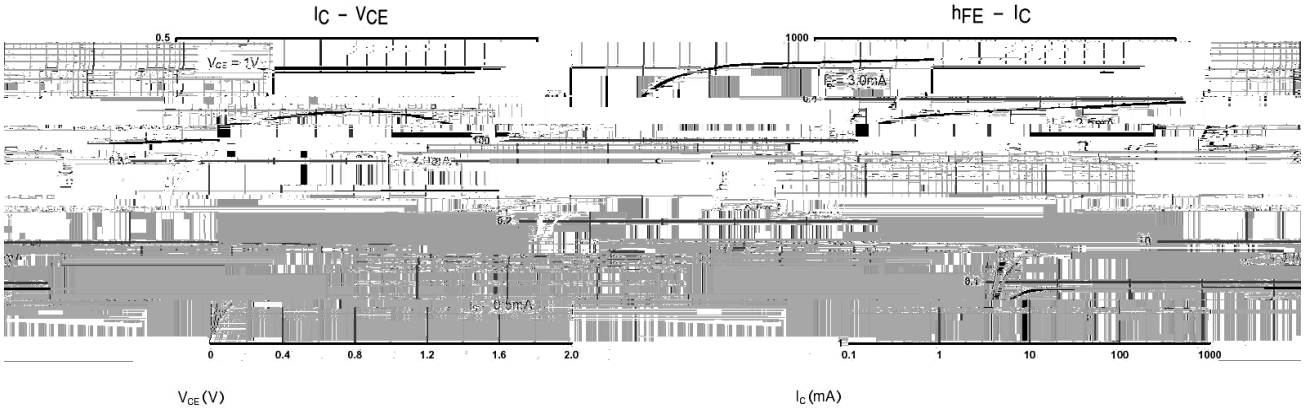


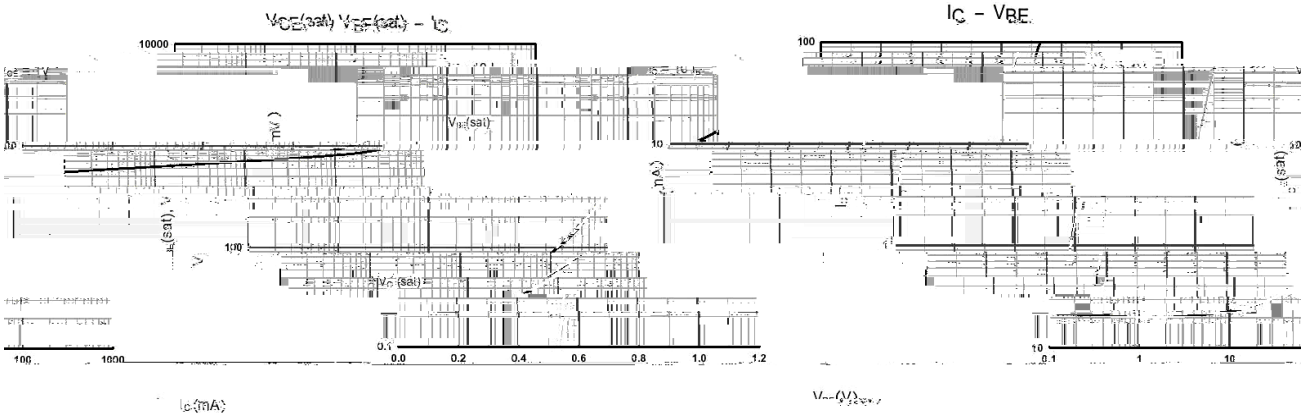
PIN1 y Base PIN 2 y Emitter PIN 3 y Collector

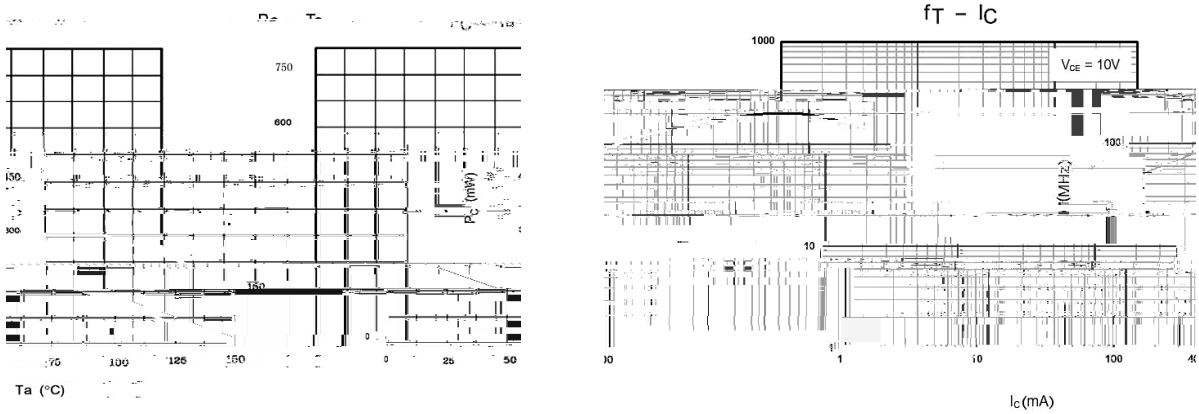
Z Ü B , M V / h_{FE} Classifications & Marking

@ *f* Parameter	… Z Symbol	*f* › Rating	% y Unit
Collector to Base Voltage	V		

Ô ? d • Ž ¢ / Electrical Characteristic Curve







Ø ¤ =) ¢ / Package Dimensions



20MIN	E	1.70	2.10	M

Tm -.522.48 http:/400640 p:/[(D 025151 3.9 -43.54 31.43.54 T 40 1 16 Tf 8.99638 -ET q9.73999208 c.com Tm42.72 .00001

š WD t ... • Ž ¢ (x /) / : K S V K X G Z [X K 6 X U L O R K L U X / 8 8 K L R U] 9 U R J K X O T M 6

ª ¢ y

1o• Ä ½ “ † 150 ½180 - k ž • 60 ½90sec;

2o• Q › “ † 245 r 5 - k ž • 4 Ò 5 r 0.5sec;

3o•D N ò i Ò 0 , † 2 ½10 - /sec.

Note:

1.Preheating:150~180 - , Time:60~90sec.

2.Peak Temp.:245 r 5 - , Duration:5 r 0.5sec.

3. Cooling Speed: 2~10 - /sec.

ÂD /Cã p10 »] / Resistance to Soldering Heat Test Conditions

“ † y 260 r 5 -	ž • y 10 r 1 sec.	Temp.:260 r 5 -	Time:10 r 1 sec

G P á / Packaging SPEC.

2 & x / REEL