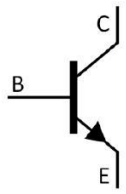
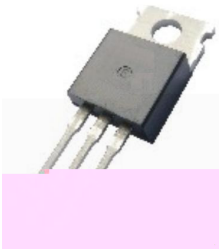
WR0553 Ṃ Ṃ Ḅ QSQ Æ Ṁ ч s ‖ ㄱ Silicon NPN transistor in a TO-220 Plastic Package.

лъ WLS65 Ӈ ´
Complement to TIP32.

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Medium power linear switching applications.



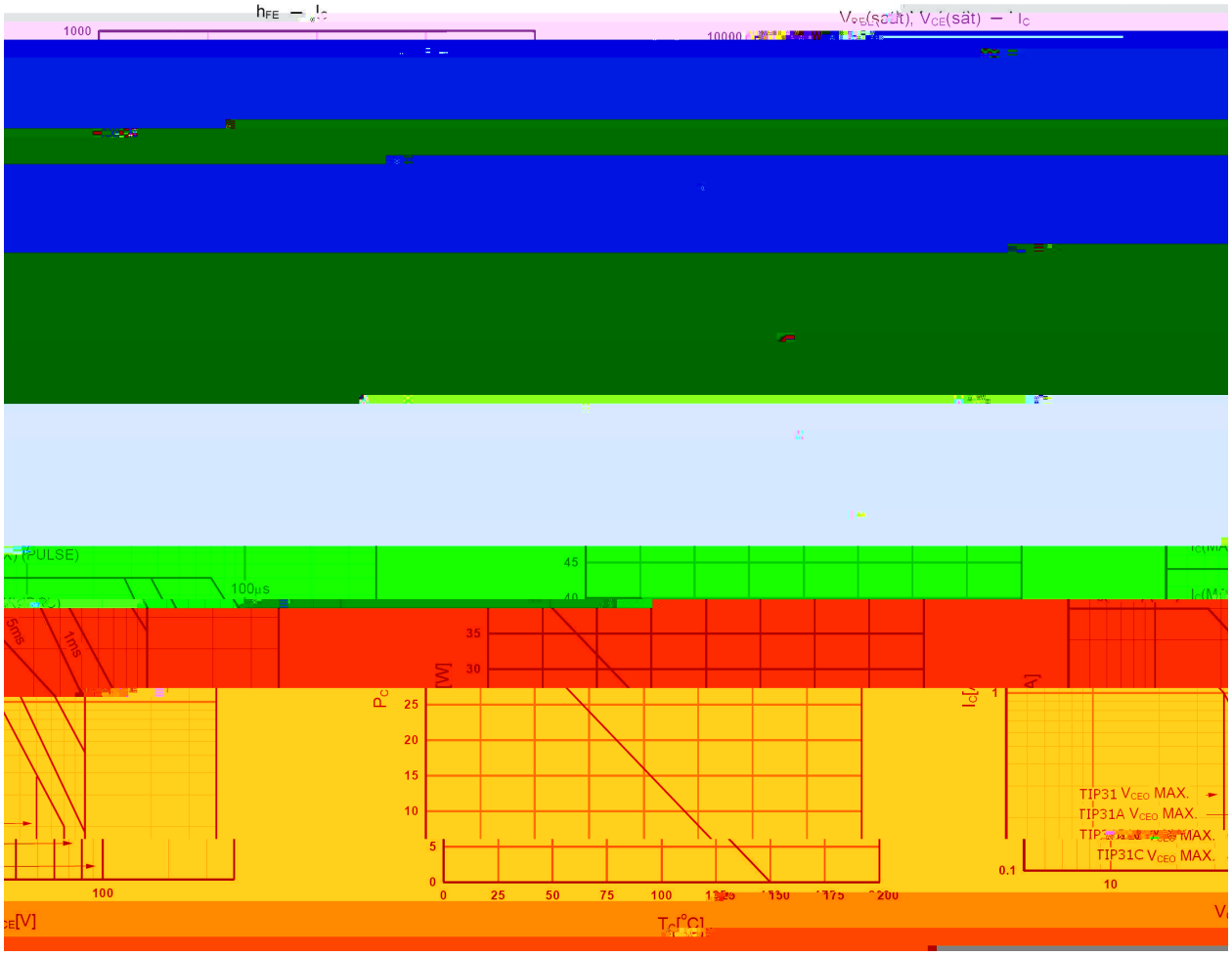



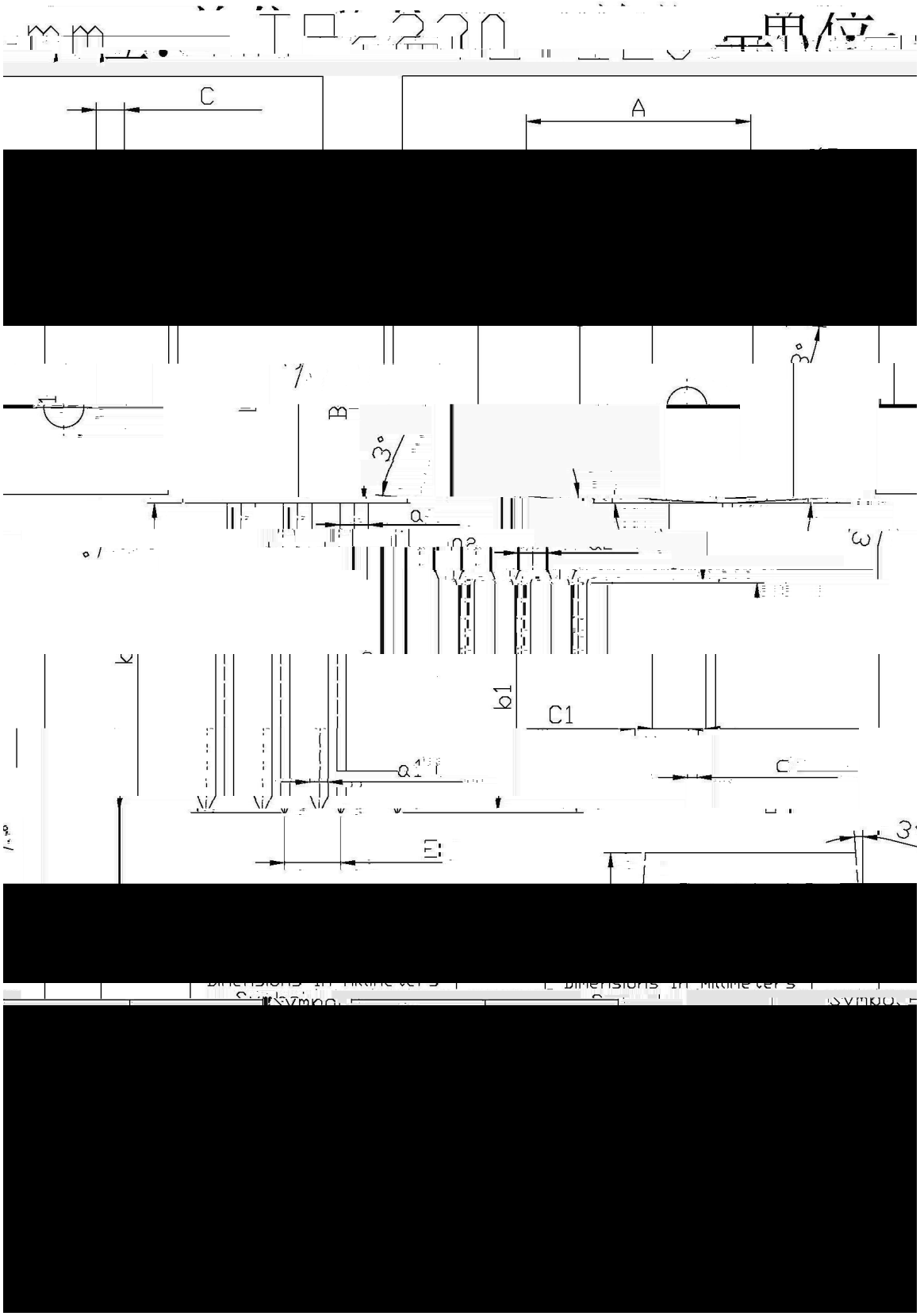
PIN1 χ Base PIN 2 χ Collector PIN 3 χ Emitter

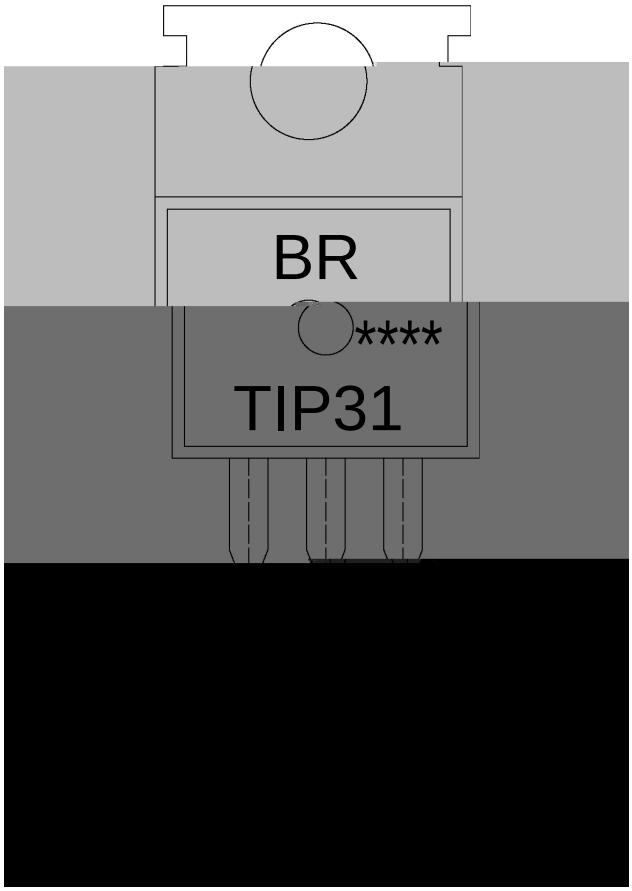
See Marking Instructions.

| v ˝Ω<br>Parameter | ϒ N<br>Symbol | ˝Ω ë<br>Rating | | | | D Ч<br>Unit |
|---|---|---|---|---|---|---|
| | | TIP31 | TIP31A | TIP31B | TIP31C | |
| Collector to Base Voltage | $V_{CBO}$ | 40 | 60 | 80 | 100 | V |
| Collector to Emitter Voltage | $V_{CEO}$ | 40 | 60 | 80 | 100 | V |
| Emitter to Base Voltage | $V_{EBO}$ | 5.0 | | | | V |
| Collector Current - Continuous | $I_C$ | 3.0 | | | | A |
| Peak Collector Current | $I_{CP}$ | 5.0 | | | | A |
| Base Current - Continuous | $I_B$ | 1.0 | | | | A |
| Collector Power Dissipation | $P_C$ | 2.0 | | | | W |
| Collector Power Dissipation | $P_C$ (Tc=25 ) | 40 | | | | W |
| Junction Temperature | $T_j$ | 150 | | | | |
| Storage Temperature Range | $T_{sag}$ | -55 150 | | | | |

v ˝Ω


1000
10000
100μs
1ms
5ms
45
40
35
30
25
20
15
10
5
0
0.1
1
100
10
0
25
50
75
100
150
175
200
TIP31 $V_{CEO}$ MAX.
TIP31A $V_{CEO}$ MAX.
TIP31C $V_{CEO}$ MAX.


C
A
B
3°
a
b1
C1
a1
E1




ᴗ ˝ χ

| | |
|---|---|
| BR χ | ѭ ϗ ᴎ Ꮁ |
| TIP31 χ | ѭ ғ β ʒ ᴺ |
| ---- χ | ѭ ı ғ ӈ̃ ᴺ Ꮁ ı ϕ ı ғ ӈ̃ ᴺ Ψ ᖘ |

Note:

| | |
|---|---|
| BR: | Company Code |
| TIP31: | Product Type. |
| ****: | Lot No. Code, code change with Lot No. |